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PATENT
8040-1022

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Hirotaka NAKANO et al. Conf. 9420

Application No. 09/855,723 Group 2825

Filed May 16, 2001 Examiner Magid Y Dimyan

SYSTEM OF MANUFACTURING SEMICONDUCTOR
INTEGRATED CIRCUIT BY HAVING A CLIENT
CONNECTED TO A MANUFACTURER VIA
TWO-WAY COMMUNICATION

INFORMATION DISCLOSURE STATEMENT

MS ISSUE FEE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the accompanying documents, copies of which are attached to this statement, are made of record on the enclosed Form PTO-1449.

A concise explanation of the relevance of these items is that these references were cited by the Japanese Patent Office in the corresponding Japanese Application Serial No. 142918/2000, filed May 16, 2000. A copy of the Japanese Official Action in which they were cited is attached hereto, with what is believed to be the pertinent portion enclosed in a wavy line. **An English translation of the enclosed portion is also attached hereto.**

Under the provisions of 37 CFR 1.97(e), the undersigned hereby certifies that each item of information

07/14/2005 HAL111 00000030 250120 09855723

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contained in this Information Disclosure Statement was first cited in any communication from a foreign Patent Office in a counterpart foreign application not more than three months prior to the filing of this Statement.

Charge the petition fee of \$180 to deposit account No. 25-0120.

Respectfully submitted,

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RJP/fb

July 12, 2005

| INFORMATION DISCLOSURE CITATION IN AN APPLICATION <small>(Use several sheets if necessary)</small> | | Attorney Docket No.: 8040-1022 | | Application No.: 09/855,723 | | | |
|---|--|---|---------|---------------------------------------|----------|-------------|----|
| | | Applicant: Hirotaka NAKANO et al. | | | | | |
| | | Filing Date: May 16, 2001 | | Group Art Unit: 2825 | | | |
| | | JUL 12 2005 U.S. PATENT DOCUMENTS | | | | | |
| Examiner Initial | Document Number <small>U.S. PATENT & TRADEMARK OFFICE</small> | Date | Name | Class | Subclass | | |
| Filing date (if appropriate) | | | | | | | |
| FOREIGN PATENT DOCUMENTS | | | | | | | |
| Examiner Initial | Document Number | Date | Country | Class | Subclass | Translation | |
| | | | | | | Yes | No |
| | 2000-122011 | 4/28/00 | Japan | | | | x |
| | 11-50320 | 2/23/99 | Japan | | | | x |
| | 11-34598 | 2/9/99 | Japan | | | | x |
| | 9-319795 | 12/12/97 | Japan | | | | x |
| | 6-41805 | 2/15/94 | Japan | | | | x |
| | 5-307579 | 11/19/93 | Japan | | | | x |
| OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) | | | | | | | |
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| EXAMINER: | | DATE CONSIDERED | | | | | |
| EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant. | | | | | | | |

* Abstract provided for the Examiner's convenience